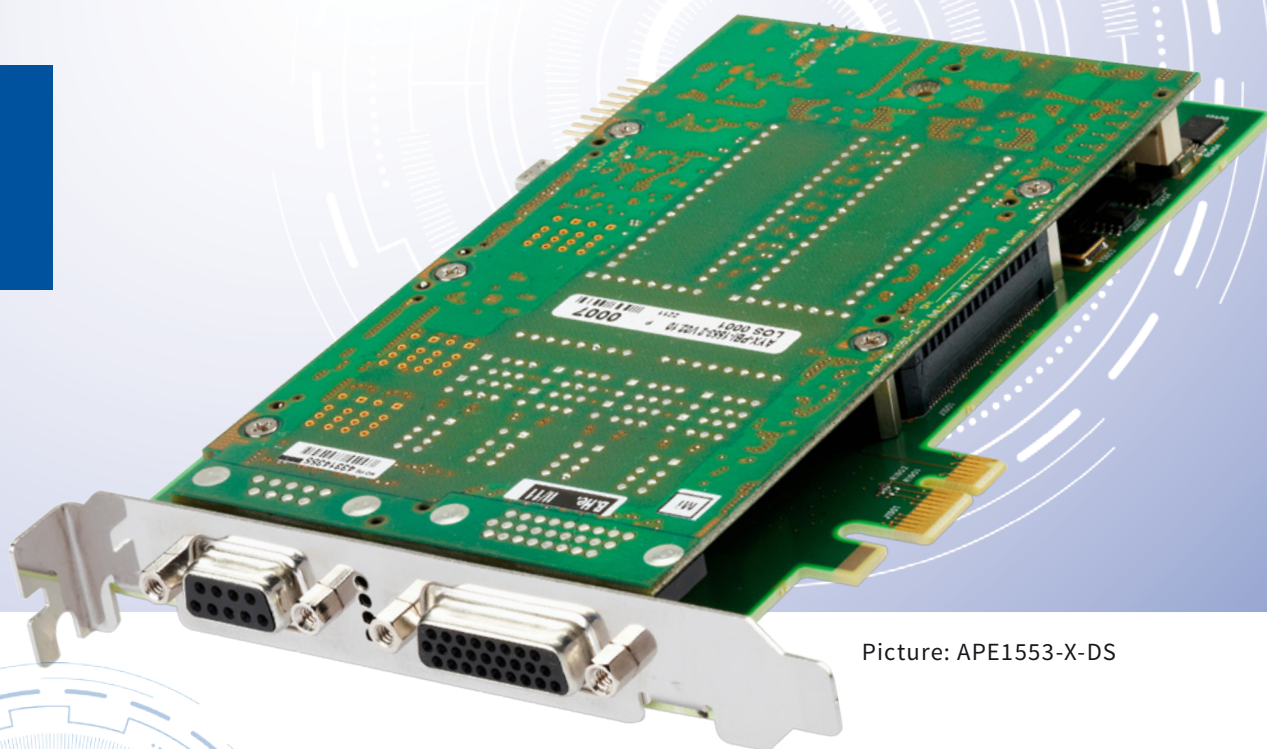


APE1553-X

Single, Dual or Quad Stream
MIL-STD-1553A/B Test & Simulation
Module for PCI Express

Data
Sheet



Picture: APE1553-X-DS

APE1553-x

Single, Dual or Quad Stream MIL-STD-1553A/B Test & Simulation Module for PCI Express

General Features

The APE1553-x is a member of AIM's new family of PCI Express modules for analysing, simulating, monitoring and testing ► **MIL-STD-1553A/B** databuses. The APE1553-x concurrently acts as Bus Controller, Multiple Remote Terminals (31) and Chronological/Mailbox Bus Monitor.

The APE1553-x-DS versions known as ► **MILScope™**, have an onboard A/D converter on the first MIL-STD-1553 stream. The MILScope™ option provides a unique capability to test and verify the MIL-STD-1553 waveform and detect faulty bus conditions without the need of an external oscilloscope.

Versions with reduced functionality (Single Function or Simulator Only) are available as well as extended temperature range variants. All APE1553-x cards have the capability to handle 8 General Purpose Discrete I/O (GPIO) signals and also offer Trigger I/O.

A full range of MIL-STD-1553 protocol errors can be injected/detected. The APE1553-x modules can electrically reconstruct and replay previously recorded MIL-STD-1553A/B record files physically to the MIL-STD-1553A/B bus with excellent timing accuracy.

The APE1553-x offers an interface for 1, 2 or 4 dual redundant bus streams. All versions are short length PCIe card formats.

The APE1553-x modules use AIM's Common Core hardware design utilizing multiple RISC processors with 128MB of Global RAM and 128MB of ASP RAM. The on-board ASP (Application Support Processor) which is based on a SoC (System on Chip) hardware device is running under Linux OS.

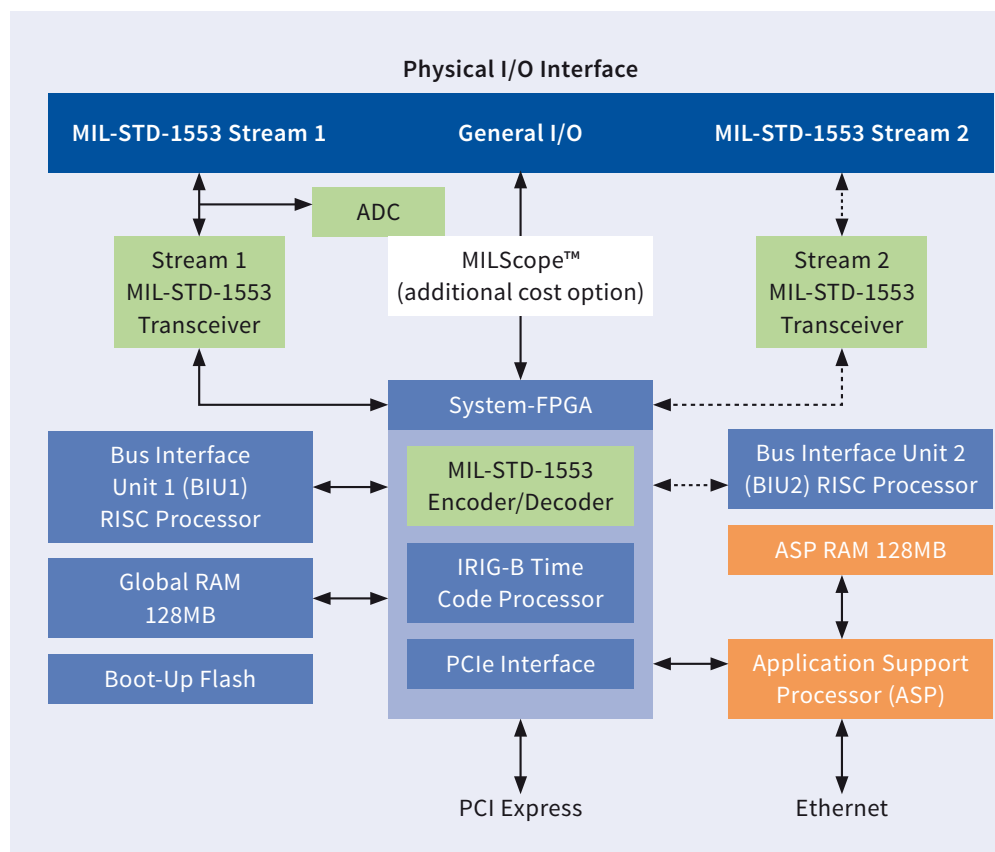
This offers a scalable and flexible platform for hosting various onboard applications. ASP application software development is supported via the onboard Standard Ethernet Interface and an optional break-out panel.

The use of onboard processing and large memory enables autonomous operation with minimal interaction with the host PC for real time applications. An onboard IRIG-B time encoder/decoder is included with sinusoidal output and free-wheeling mode for time tag synchronization on system level using one or more APE1553-x cards. The Physical Bus Interface (PBI) provides programmable bus coupling modes and variable output amplitude to the MIL-STD-1553A/B bus.

Full function driver software is delivered with the APE1553-x cards in comprehensive Board Software Packages (BSPs) for different operating systems.

The optional ► **PBA.pro™** Databus Test and Analysis Tool (for Windows and Linux) can also be purchased for use with APE1553-x modules. PBA.pro™ software components are available to support the MILScope™ capability of APE1553-x-DS cards to view and verify the MIL-STD-1553 waveform.

Off the shelf test scripts are available to support the automatic execution of the AS4112 RT Production Test Plan (Protocol and Electrical Tests) and the AS4111 RT Validation Test Plan (Protocol Tests).



APE1553-1/2-DS Block Diagram

APE1553-4
Block Diagram

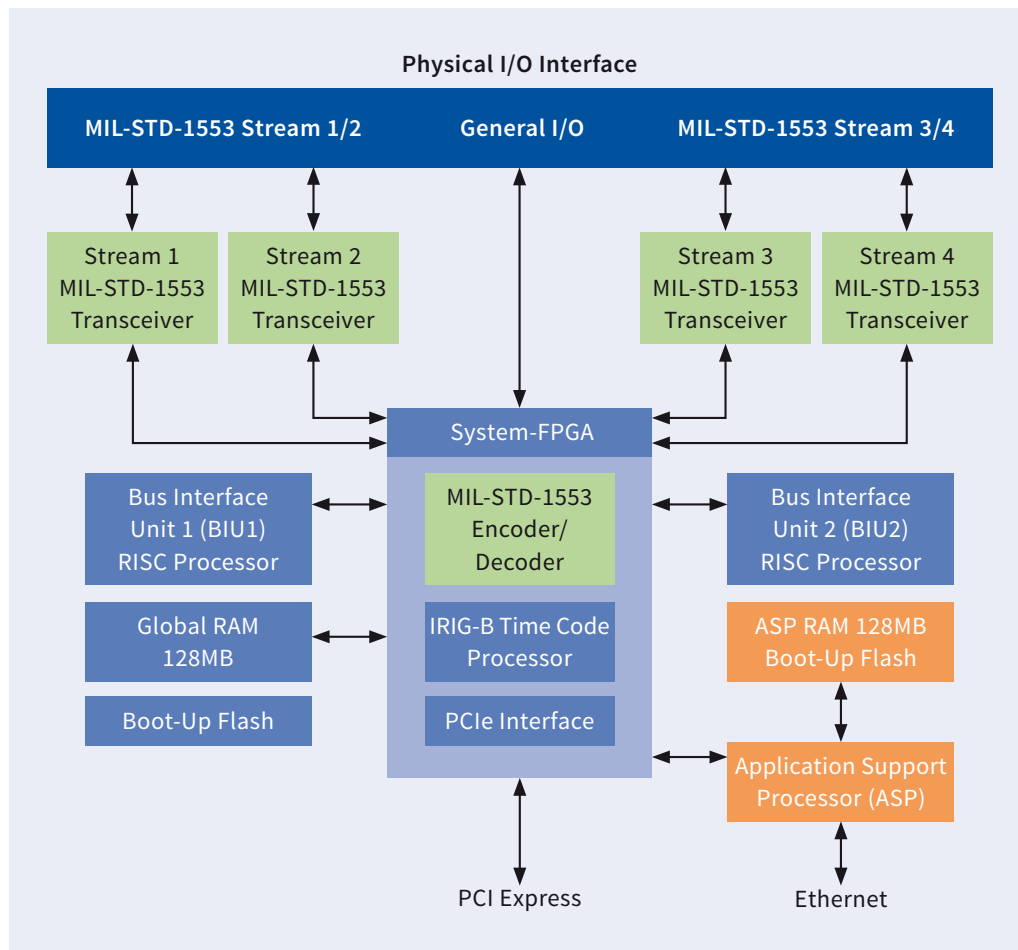
Bus Controller

The ► **APE1553-x** modules provide real time Bus Controller functions on each independent, dual redundant ► **MIL-STD-1553A/B** Databus stream, concurrently with Multiple RT and Chronological Bus Monitor operation.

Two 400MHz RISC processors, one for each Single or Dual Stream Bus Interface Unit, provide true simulation of BC operations without host computer interaction.

Key Features of the Bus Controller Mode include:

- Autonomous Operation including Sequencing of Minor/Major Frames
- Acyclic Message Insertion/Deletion
- Programmable BC Retry without Host Interaction
- Full Error Injection down to Word and Bit Level
- Multi-Buffering with Real Time Data Buffer Updates
- Synchronization of BC Operation to external Trigger Inputs
- 4µs Intermessage Gaps
- Interrupt Generation on BC Transfer Events
- Start on external Trigger Input



Multiple Remote Terminal

The APE1553-x modules simulate up to 31 Remote Terminals, including all sub addresses on each MIL-STD-1553 stream, concurrently with BC and Chronological Monitor operation.

Alternatively each of the 31 RT's can operate in message oriented Mailbox Monitor Mode to monitor Non-Simulated RT's.

Key Features of the Remote Terminal Simulation Mode include:

- Programmable RT Response Time down to 4µs for each simulated RT
- Programmable and Intelligent Response to Mode Codes
- Full Error Injection down to Word and Bit Level (AS4112 compliant)
- Multi-Buffering with Real Time Data Buffer Updates
- Mailbox Monitor Mode
- Interrupt Generation on RT Events

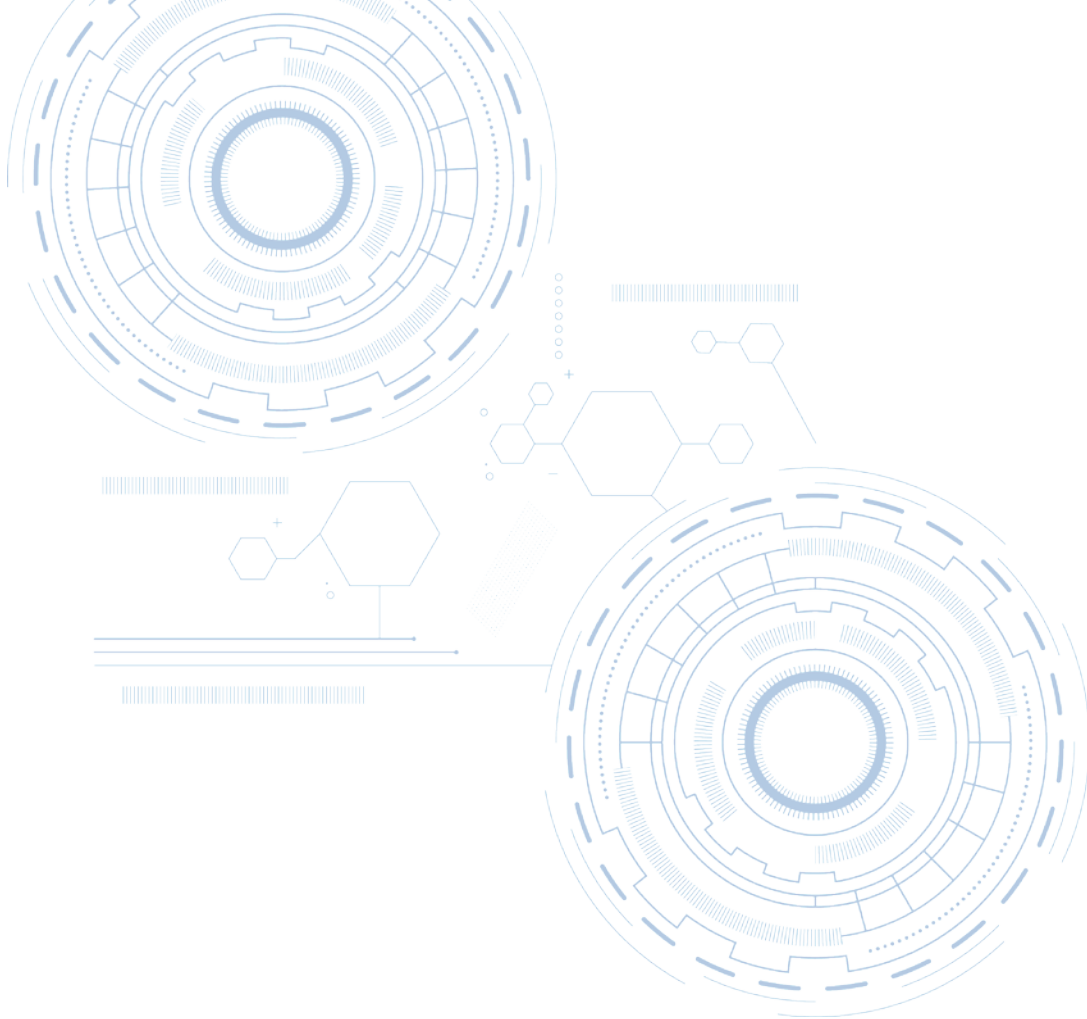
Physical Bus Replay

The APE1553-x cards can electrically reconstruct and replay previously recorded MIL-STD-1553A/B record files physically to the MIL-STD-1553A/B bus with excellent timing accuracy.

Record files can be selected for Bus Replay. The additional capability to disable any or all RT responses from the MIL-STD-1553A/B replay enables smart system integration and test to be performed.

Physical Bus Interface

A Physical Bus Interface (PBI) daughter board provides software programmable transformer or direct coupling with software programmable variable output transceivers and a terminated bus network to enable the direct connection of a single BC or RT device. The coupling to the external bus is software programmable.



Chronological Bus Monitor

The APE1553-x modules provide full bus monitoring and analysis with time tagging of all bus traffic with 1 μ s resolution including response time and gap time measurement down to 250ns concurrently with BC and Multi RT operation.

Key Features of the Chronological Bus Monitor include:

- 100% Data Capture on each stream at full Bus Rates
- Single Shot, Continuous or Selective Capture Modes
- Autonomous Message Synchronization and Full Error Detection
- 2 Static/Dynamic Complex Triggers with Sequencing
- Message Filter and Selection Capture
- Bus Activity Recording independent from Trigger and Capture Mode
- Time Tagging:
 - All Bus Traffic to 1 μ s
 - Intermessage Gaps and Response Time to 250ns
- External Trigger Outputs
- Programmable Response Timeout

MILScope™ (available as a cost option)

The APE1553-x-DS versions integrate on 1 stream of the PBI a 2 channel differential Analog to Digital Converter (ADC) providing up to 50Msamples for primary and secondary data acquisition.

Accurate measurements of physical bus parameters such as rise/fall time, overshoot, undershoot, pulse width and amplitude, can be triggered by the complex trigger of the Bus Monitor.

Trigger-/General Purpose Discrete I/O Signals

The Front-I/O connectors provide 1 BC-, RT- and BM-trigger input and 1 BC-, RT- and BM-trigger output for each MIL-STD-1553A/B stream. Additionally up to 5 user programmable General Purpose Discrete I/O signals can be accessed via Front-I/O. All 8 onboard General Purpose Discrete I/O signals, which are user programmable for input or output can be accessed via the board-to-board ribbon cable connector. Voltage levels of all trigger signals and General Purpose Discrete I/O's are TTL compatible whereas the General Purpose Discrete I/O's are designed to handle avionics level as well.

IRIG-B Time Encoder/Decoder

APE1553-x modules include an onboard IRIG-B time encoder/decoder with sinusoidal output and free-wheeling mode for time tag synchronization.

This allows synchronization of multiple APXE1553-x modules to 1 common IRIG-B time input source or to the onboard time code generator of 1 APXE1553-x module as the reference for correlation of data across multiple MIL-STD-1553A/B streams.

Driver Software

The Driver Software is supplied with the APE1553-x module. A full function Application Programming Interface (API) is provided compatible Windows and Linux. Host applications can be written in C, C++ and Python. A LabView/VI application interface driver is provided.

Technical Data

System Interface

Single Lane (PCIe x1),
2.5Gb/s PCI Express V1.1 compliant;
compatible to higher versions

Processors

1x or 2x 400MHz RISC Processors
for BIU(s) and a 400MHz Application
Support Processor (ASP)

Memory

128MB Global RAM (DDR2-RAM),
128MB Global RAM (DDR-RAM),
2x 8MBit serial flash memory for BIUs,
64MBit serial flash memory for LCA

Encoder/Decoder

Up to 4 MIL-STD-1553A/B Encoders/
Decoders with full error injection and
detection

Time Tagging

Sinusoidal 46-bit absolute IRIG-B Time
stamping with 1µs resolution

Trigger/General Purpose Discretes

1x BC-, RT- and BM-Trigger input and
1x BC-, RT- and BM-Trigger output for each
stream available with up to 5 General
Purpose Discrete I/O's (avionics level)
on the front panel connector

Physical Bus Interface

1, 2 or 4 MIL-STD-1553A/B Transceivers with
variable Output Amplitude, Programmable
Bus Coupling modes with onboard
terminated Bus Network

Connectors

PCIe bus standard edge Connector
APE1553-1/2:

9-way D-Sub for Bus connections,
26-way High Density D-Sub for Trigger,
General Purpose Discrete I/O and
IRIG Time Code I/O

APE1553-4:

2x 15-way High Density D-Sub for Bus
connections, Trigger, General Purpose
discrete I/O and IRIG Time Code I/O

Dimensions

167.65mm x 111.15mm

Ordering Information

APE1553-1/2

Single Stream, Dual Redundant PCIe
bus to MIL-STD-1553A/B Interface:
BC, Multi RT Simulator with Mailbox &
Chronological Monitor; IRIG-B Time
Encoder/Decoder, 8 General Purpose
Discrete I/O's (5 on Front I/O, 8 on
board-to-board connector),
128MB Global RAM, 128MB ASP RAM

APE1553-1-DS

Single Stream, Dual Redundant PCI-X
bus to MIL-STD-1553A/B Interface:
BC, Multi RT Simulator with Mailbox &
Chronological Monitor;
IRIG-B Encoder/Decoder, 8 General
Purpose Discrete I/O's (5 on Front I/O,
8 on B2B Connector); Digitizing Scope
for Waveform Analysis & Measurement;
1MB Global RAM, 128MB ASP RAM;
Short Length Card

APE1553-2-DS

Dual Stream, Dual Redundant PCIe bus
to MIL-STD-1553A/B Interface:
BC, Multi RT Simulator with Mailbox &
Chronological Monitor; IRIG-B Time
Encoder/Decoder, 8 General Purpose
Discrete I/O's (5 on Front I/O,
8 on board-to-board connector),
128MB Global RAM, 128MB ASP RAM

APE1553-4

Quad Stream, Dual Redundant PCIe bus
to MIL-STD-1553A/B Interface:
BC, Multi RT Simulator with Mailbox &
Chronological Monitor; IRIG-B Time
Encoder/Decoder, 8 General Purpose
Discrete I/O's (2 on Front I/O,
8 on board-to-board connector),
128MB Global RAM, 128MB ASP RAM

Simulator Only versions available

BC, Multi RT Simulator with Mailbox
Monitor

Single Function versions available

Chronological Monitor & Mailbox
Monitor OR Bus Controller OR Multi RT
and Mailbox Monitor

ACB-PCI-1

Ready Made Adapter Cable (2.0m):
From D-Sub to 2 Twinax Connectors
PL-75 for all variants of APXE1553-1 cards

ACB-PCI-2

Ready Made Adapter Cable (2.0m):
From D-Sub to 4 Twinax Connectors
PL-75 for all variants of APXE1553-2 cards

ACB-HD15-2

Ready Made Adapter Cable (2.0m):
From 15-pin HD-Sub to 4 Twinax
Connectors PL-75 for all variants of
APXE1553-4 cards

Note: For all variants of APXE1553-4
cards use 2 of ACB-HD15-2/
ACB-HD15-2-F Adapter Cables

ACB-HD15-2-F

Ready Made Adapter Cable (2.0m):
From 15-pin HD-Sub to 4 Twinax
Connectors PL-75 and 9-pin D-Sub
Connector for Trigger-I/O, IRIG-B
and Discrete I/O's for all variants of
APXE1553-4 cards

Power Consumption

APE1553-1:

typical 2.8W @3.3VDC, 2.7W @12VDC

APE1553-2:

typical 2.9W @3.3VDC, 4.8W @12VDC

APE1553-4:

typical 4.5W @3.3VDC, 8.5W @12VDC
(@50% busload)

Operating Temperature Range

Standard: 0°C to +45°C ambient

Extended: -15°C...+60°C

Storage Temperature

-40°C to +85°C

Humidity

0 to 95% non-condensing

► AIM Office Contacts:

AIM GmbH

Sasbacher Str. 2
D-79111 Freiburg / Germany
Phone +49 (0)761 4 52 29-0
Fax +49 (0)761 4 52 29-33
sales@aim-online.com

AIM GmbH – Munich Sales Office

Terofalstr. 23a
D-80689 München / Germany
Phone +49 (0)89 70 92 92-92
Fax +49 (0)89 70 92 92-94
salesgermany@aim-online.com

AIM UK Office

Cressex Enterprise Centre, Lincoln Rd.
High Wycombe, Bucks. HP12 3RB / UK
Phone +44 (0)1494-446844
Fax +44 (0)1494-449324
salesuk@aim-online.com

AIM USA LLC

Seven Neshaminy Interplex
Suite 211 Trevose, PA 19053
Phone 267-982-2600
Fax 215-645-1580
sales@aim-online.us